

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re <b>Divisional</b> Application of:	)
Yukiko NAKAOKA et al	) Attn: Applications Branch
Based on US Serial No. 10/062,535	) Group Art Unit: 2829
Which Was Filed: July February 5, 2002	)
For: SEMICONDUCTOR DEVICE AND	)
MANUFACTURING METHOD THEREOF	) July 18, 2003

**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

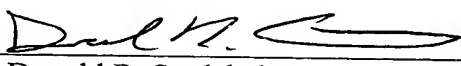
Sir:

In accordance with the duty of disclosure as set forth in 37 C.F.R. §1.56, Applicants hereby submit the following information in conformance with 37 C.F.R. §§ 1.97 and 1.98. Pursuant to 37 C.F.R. § 1.98. The references cited on the PTO-1449 Form were cited in the patent of the above-captioned application.

It is requested that the accompanying PTO-1449 be considered and made of record in the above-identified application. To assist the Examiner, the documents are listed on the attached form PTO-1449. It is respectfully requested that an Examiner initialed copy of this form be returned to the undersigned.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 19-2380.

Respectfully submitted,

By:   
Donald R. Studebaker  
Registration No. 27,997

NIXON PEABODY LLP  
8180 Greensboro Drive, Suite 800  
McLean, Virginia 22102

Telephone: (703) 770-9300

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Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

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